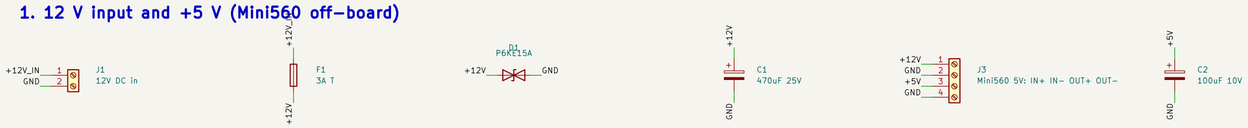
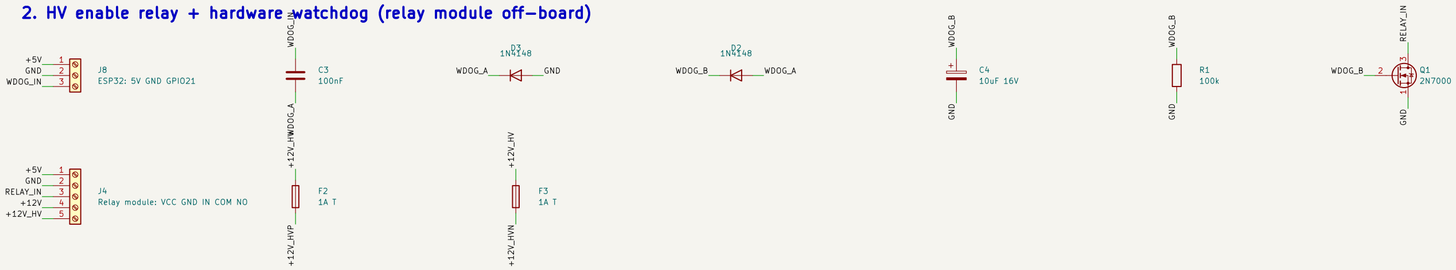
Plasma DMD – 12 V DC PSU carrier board (rev 0.1, unverified)
Connectivity is by net labels: every pin has a stub + label. Off-board modules connect via J3–J7.
Panel voltages are set by zeners D5 (68 V -> about +65 V) and D7+D8 (113 V -> about −112 / −100 V), copied from Stern 520-5138-00.
SAFETY: keep >= 3 mm clearance (more is better) between HV nets (+90V_RAW, +65V, −130V_RAW, −112V, −100V, HVN_MOD, HVP_MOD) and low-voltage nets.
J2 pin order follows the Stern CN2 connector: map it to YOUR panel before use. Pin 3 is the key position.

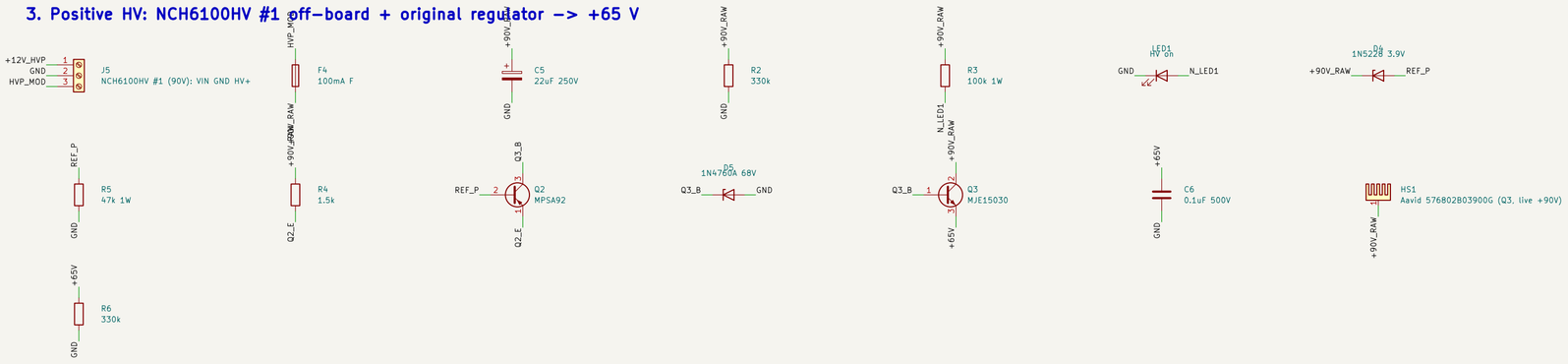
1. 12 V input and +5 V (Mini560 off-board)



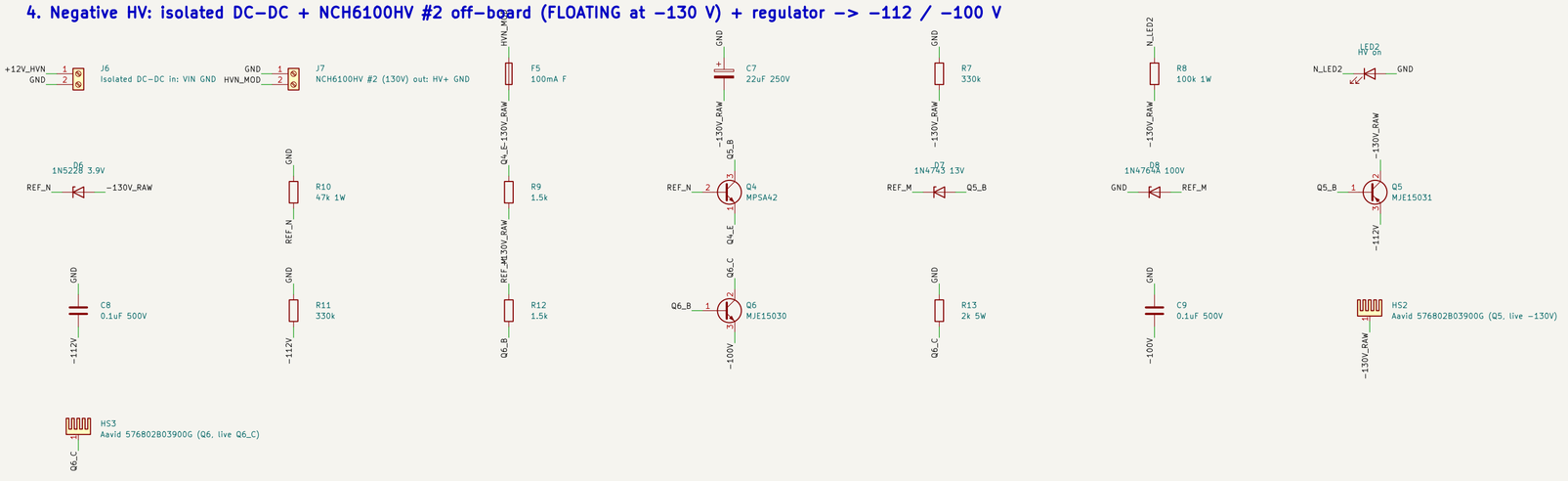
2. HV enable relay + hardware watchdog (relay module off-board)



3. Positive HV: NCH6100HV #1 off-board + original regulator -> +65 V



4. Negative HV: isolated DC–DC + NCH6100HV #2 off-board (FLOATING at −130 V) + regulator -> −112 / −100 V



5. Panel connector and mounting

Unverified – see notes

Sheet: /	
File: plasma_dmd_psu.kicad_sch	
Title: Plasma DMD 12 V DC PSU	
Size: A2	Date:
KiCad E.D.A. 10.0.6	**Rev: 0.1**
	Id: 1/1